AN2045

ES32VF2264

©

V1.00	2023-12-12	
V1.01	2024-04-18	1. 2. CDK 3. I2C UART SPI
V1.02	2024-07-29	1. GPIO IO 2. SPI SPI 3. DMA
V1.03	2024-09-11	1. IAP FLASH
V1.04	2024-10-11	1. 32pin SPI1
V1.05	2024-11-29	1. DMA
V1.06	2025-07-10	1. 2. DMA ADC DMA

1515 12 3

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1 4
1. 1 4
1. 2 5
1. 3 5
1. 4 HOSC 5
1. 5 5
1. 5. 1 6
1. 5. 2 IAP 6
1. 5. 3 IWDT 6
1. 5. 4 WWDT 6
1. 6 IAP 6
1. 7 GPIO .....É..Ô.....

©

1

1. 1

ES-Burner +ES-LinkII-OB/PRO

GBRDP Level0

GBRDP

1-1

©

1. 2

CDK

CDK https://www.xrvm.cn/community/download CDK

V2.20.0 CDK

1-2 CDK

CDK _CDK

1. 3

ES32 2 ALD MD

z ALD API

z MD

1. 4 HOSC

1 12MHz CMU_CLKENR HOSCFBPS

1

1. 5

WDT

©

1. 5. 1

SYSCFG_PROT

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01

1. 8 IWDT

1 IWDT IWDT

2 IWDT IWDTEN
IWDT

3 IWDT LRC
0.5 IWDT_LOAD IWDT
IWDT IWDT_CON CLKS
RSTEN IE EN

5 IWDT

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I2C_CON2.START

1. 11 UART

1:

(UART_RIF.ABTO) (UART_MCON.ABREN)
(UART_MCON.ABRREPT)
(UART_MCON.ABREN)

2:

TFEMPTY STAT TFEMPTY 1
TFEMPTY UART_TXBUF

RFNEMPTY STAT RFNEMPTY 1
RFNEMPTY UART_RXBUF

1. 12 SPI

1 FIFO

SPI FIFO FIFO (SPI_IFM.TXE) FIFO
(SPI_IFM.TXTH) SPI_DATA
SPI_DATA FIFO
:

y 1: FIFO SPI_STAT.TXFLV FIFO
(Bytes)

y 2: FIFO SPI_STAT.TXF FIFO

2 FIFO

SPI FIFO FIFO (SPI_IFM.RXNE) FIFO
(SPI_IFM.RXF) FIFO (SPI_IFM.RXTH) FIFO
SPI RXF RXTH
( )SPI_DATA FIFO :

y 1: FIFO SPI_STAT.RXFLV FIFO Bytes

y 2: FIFO SPI_DATA SPI_STAT.RXNE 0
FIFO

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3

SPI

©

2 ADC IREFEN=1 ADC STOP ADC IREFEN=0

3 ADC PWRMODSEL=0

4 AIN19 VREF1.2V ADC

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DMA ADC DMA ADC DMA
CRC->DATA PIS -> AD16C4T ITR -

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z IRQ csi_vic_disable_sirq (XXX_IRQn)

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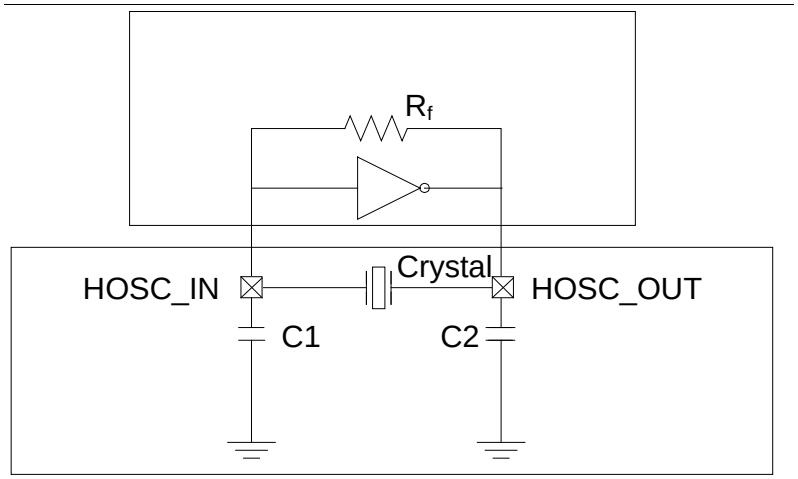
2

2. 1



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